EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	273	pld and crossbar and switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 07:49
L2	296	(pld or fpga or (programmable adj logic adj device)) and crossbar and switch and latency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 07:50
L3	202	(pld or fpga or (programmable adj logic adj device)) and crossbar and switch and latency and delay	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 07:50
L4	167	(pld or fpga or (programmable adj logic adj device)) and crossbar and switch and latency and delay and cycle	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 07:51
L5	146	(pld or fpga or (programmable adj logic adj device)) and crossbar and switch and latency and delay and cycle and input and output	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 07:51
L6	107	(pld or fpga or (programmable adj logic adj device)) and crossbar and switch and latency and delay and cycle and input and output and stage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:06
L7	58	(pld or fpga or (programmable adj logic adj device)) and crossbar and switch and latency and delay and cycle and input and output and stage and (reconfigur$3 or reprogram$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:07
L8	58	(pld or fpga or (programmable adj logic adj device)) and crossbar and switch and latency and delay and cycle and input and output and stage and (reconfigur$3 or reprogram$4) and number	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:10

L9	0	(pld or fpga or (programmable adj logic adj device)) and crossbar and switch and latency and delay and cycle and input and output and stage and (reconfigur$3 or reprogram$4) and ("same adj number adj2 clock adj cycle")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:11
L10	0	(pld or fpga or (programmable adj logic adj device)) and crossbar and switch and latency and delay and cycle and input and output and stage and (reconfigur$3 or reprogram$4) and ("equal adj number adj2 clock adj cycle")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:11
L11	0	(pld or fpga or (programmable adj logic adj device)) and crossbar and switch and latency and delay and cycle and input and output and stage and (reconfigur$3 or reprogram$4) and ("(equal$2 or same) adj number adj2 clock adj cycle")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 08:12
S1	220	pld and crossbar	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 07:48
S2	14	pld and crossbar and multi-stage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/03 09:03
S3	137	crossbar same multi-stage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/03 08:55
S4	41	crossbar same multi-stage same input same output	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/03 09:01
S5	8	crossbar same multi-stage same input same output same select$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/03 08:56

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S6	0	S1 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/03 09:02
S7	0	pld and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/03 09:02
S8	1	clb and S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/03 09:03
S9	563	(pld or fpga) and crossbar	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 10:33
S10	108263	(clb or le) and interconnect$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 10:34
S11	149	S9 and S10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 10:34
S12	74	reconfigur$3 adj4 pld	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:52
S13	43686	crossbar	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:45

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S14	5	S12 and S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:42
S15	1608	crossbar same stage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:45
S16	0	crossbar same stage same partially same reconfigur$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:46
S17	4	crossbar same partially same reconfigur$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:46
S18	0	crossbar same (configur$3 adj3 block) same reconfigur$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:50
S19	18	crossbar same block same reconfigur$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:51
S20	123	reconfigur$3 adj7 (programmable adj4 logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:55
S21	4	reconfigur$3 same (programmable adj4 logic) same crossbar	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:53

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S22	4865	crossbar adj switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 12:41
S23	115	reconfigur$3 same (crossbar adj switch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 13:09
S24	9	reconfigur$3 same (crossbar adj switch) same (partial$2 or portion)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 12:42
S25	13252	pld	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 13:09
S26	107	S22 and S25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/07 13:09
S27	63	(pld and crossbar) and reconfigur$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 09:52
S28	1633565	multi-stage or "stages"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 09:52
S29	33	S27 and S28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 09:57

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S30	2007	(716/1,14,16).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 09:57
S31	70	(crossbar adj switch) same (multi-stage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 09:58
S32	0	S30 and S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 09:58
S33	3357	(716/1,12-14,16-17).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/20 07:19
S34	3202	(326/38-41).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/20 07:19
S35	3561	(370/389,398).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/20 07:19
S36	642	(710/316).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/20 07:20
S37	2580	(713/1,100).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/20 07:20

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L1	0	((pld or (programmable adj logic adj device)) and crossbar and switch and configurable and (partially or dynamically or portion) and portion and stage).clm.	US-PGPUB; USPAT	OR	ON	2006/08/09 07:46
S1	0	((pld or (programmable adj logic adj device)) and crossbar and switch and configurable and (partially or dynamically or portion) and portion and stage and first and second and third).clm.	US-PGPUB; USPAT	OR	ON	2006/08/09 07:46

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